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RESPONSE UNDER 37 C.F.R. 1.116 - EXPEDITED
PROCEDURE - EXAMINING GROUP 2813
PATENT

Attorney Docket No. 5649-874

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Hag-ju Cho
Serial No.: 09/893,035
Filed: June 27, 2001

Group Art Unit: 2813
Examiner: Erik J. Kielin
Confirmation No.: 3421

For: METHODS OF MANUFACTURING INTEGRATED CIRCUIT DEVICES
THAT INCLUDE A METAL OXIDE LAYER DISPOSED ON ANOTHER
LAYER TO PROTECT THE OTHER LAYER FROM DIFFUSION OF
IMPURITIES AND INTEGRATED CIRCUIT DEVICES
MANUFACTURED USING SAME

Date: November 12, 2003

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT AFTER FINAL

Sir:

Applicants provide the present Request For Reconsideration to address the issues raised in the Official Action mailed September 11, 2003 ("the Action"). Applicants provide the present Request For Reconsideration pursuant to the rules stated in revised 37 C.F.R. 1.121 that became effective on July 30, 2003.

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